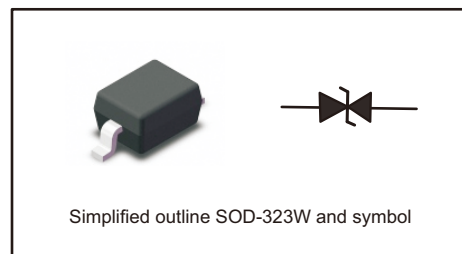


Transient Voltage Suppressors for ESD Protection

General Description

The ESDBLC5V0D3LP is designed to protect voltage sensitive components from ESD and transient voltage events. Excellent clamping capability, low leakage, and fast response time, make these parts ideal for ESD protection on designs where board space is at a premium.



Features

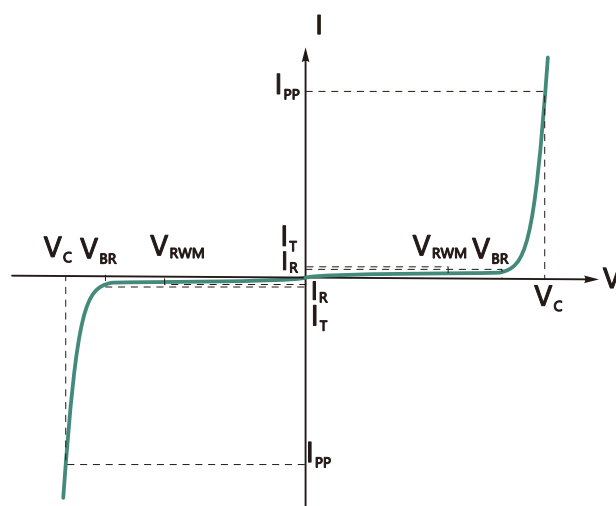
- 240 Watts peak pulse power ($t_p = 8/20\mu s$)
- Uni-directional ESD protection of one line
- IEC 61000-4-2 (ESD) $\pm 30kV$ (air), $\pm 30kV$ (contact)
- Suitable replacement for MLV's in ESD protection applications
- Working voltages: 5V
- Low leakage current
- Low clamping voltage

APPLICATIONS

- Computers and peripherals
- Audio and video equipment
- Cellular handsets and accessories
- Portable electronics
- Other electronics equipments communication systems

Electronics Parameter

| Parameter | Symbol |
|-------------------------------------|-----------|
| Maximum Reverse Peak Pulse Current | I_{PP} |
| Clamping Voltage @ I_{PP} | V_C |
| Peak Reverse Working Voltage | V_{RWM} |
| Reverse Leakage Current @ V_{RWM} | I_R |
| Breakdown Voltage @ I_T | V_{BR} |
| Test Current | I_T |





Absolute Ratings

(Tamb=25°C)

| Parameter | Symbol | Value | Unit |
|--------------------------------------------|------------------|-------------|------|
| IEC 61000-4-2 ESD Voltage Air Model | V _{ESD} | ±30 | KV |
| IEC 61000-4-2 ESD Voltage Contact Model | | ±30 | |
| Peak Pulse Power(tp=8/20us) | P _{PK} | 240 | W |
| Peak Pulse Current(tp=8/20us) | I _{PP} | 20 | A |
| Maximum junction temperature | T _J | -55 to +150 | °C |
| Storage Temperature | T _{STG} | -55 to +150 | °C |

Electrical Characteristics

Ratings at 25 °C ambient temperature unless otherwise specified.

| Parameter | Symbol | Conditions | Min | Typ | Max | Units |
|---------------------------|--------|--------------------------------|-----|-----|-----|-------|
| Reverse Stand-off voltage | VRWM | | | | 5 | V |
| Reverse Breakdown Voltage | VBR | IT=1mA | 6 | | | V |
| Reverse Leakage Current | IR | VRWM=5V | | | 0.2 | uA |
| Clamping Voltage | VC | I _{PP} =1A,tp=8/20us | | 8 | 10 | V |
| Clamping Voltage | VC | I _{PP} =20A,tp=8/20us | | 12 | | V |
| Junction Capacitance | Cj | VR=0V,f=1MHz | | 1 | | pF |



Fig.1 8 X 20us Pulse Waveform

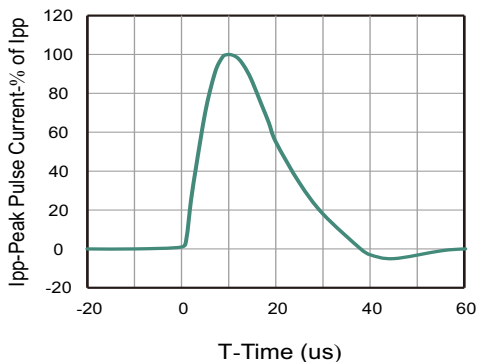


Fig.2 Power Derating Curve

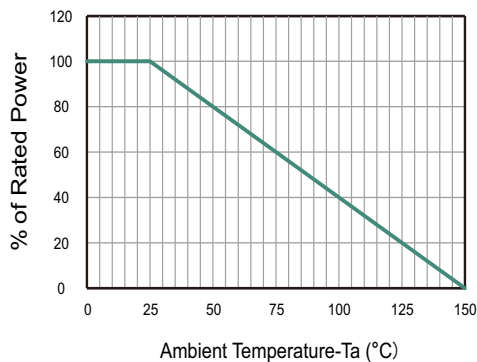


Fig.3 Clamping Voltage vs. Peak Pulse Current

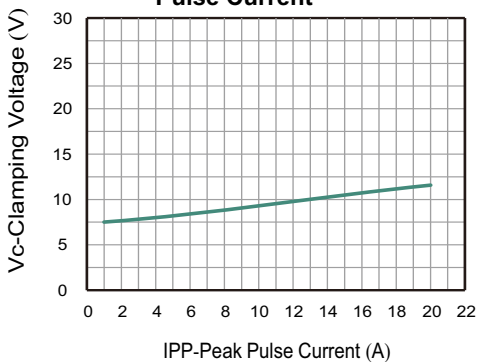


Fig.4 Junction Capacitance vs. Reverse Voltage

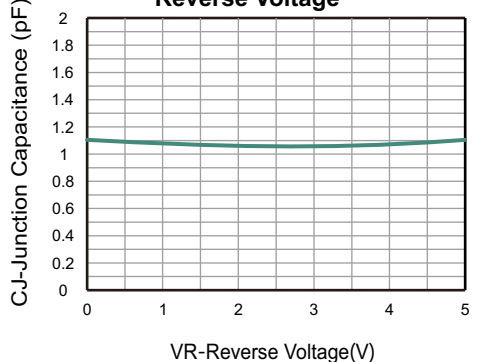


Fig.5 Peak Pulse Power vs. Pulse Time

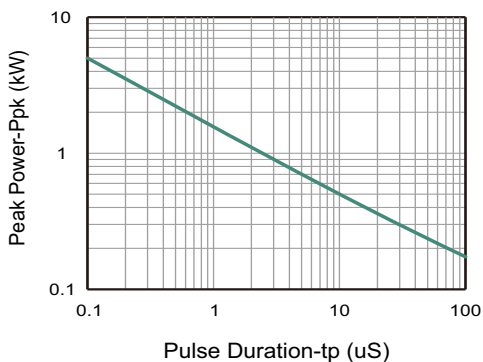
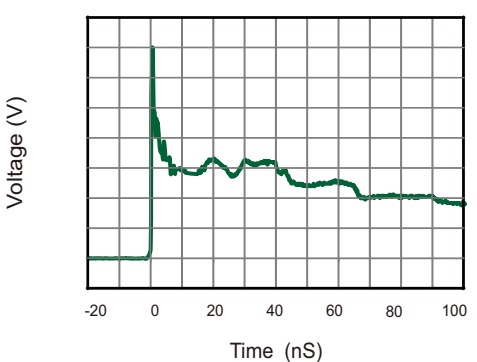


Fig.6 IEC61000-4-2 Pulse Waveform

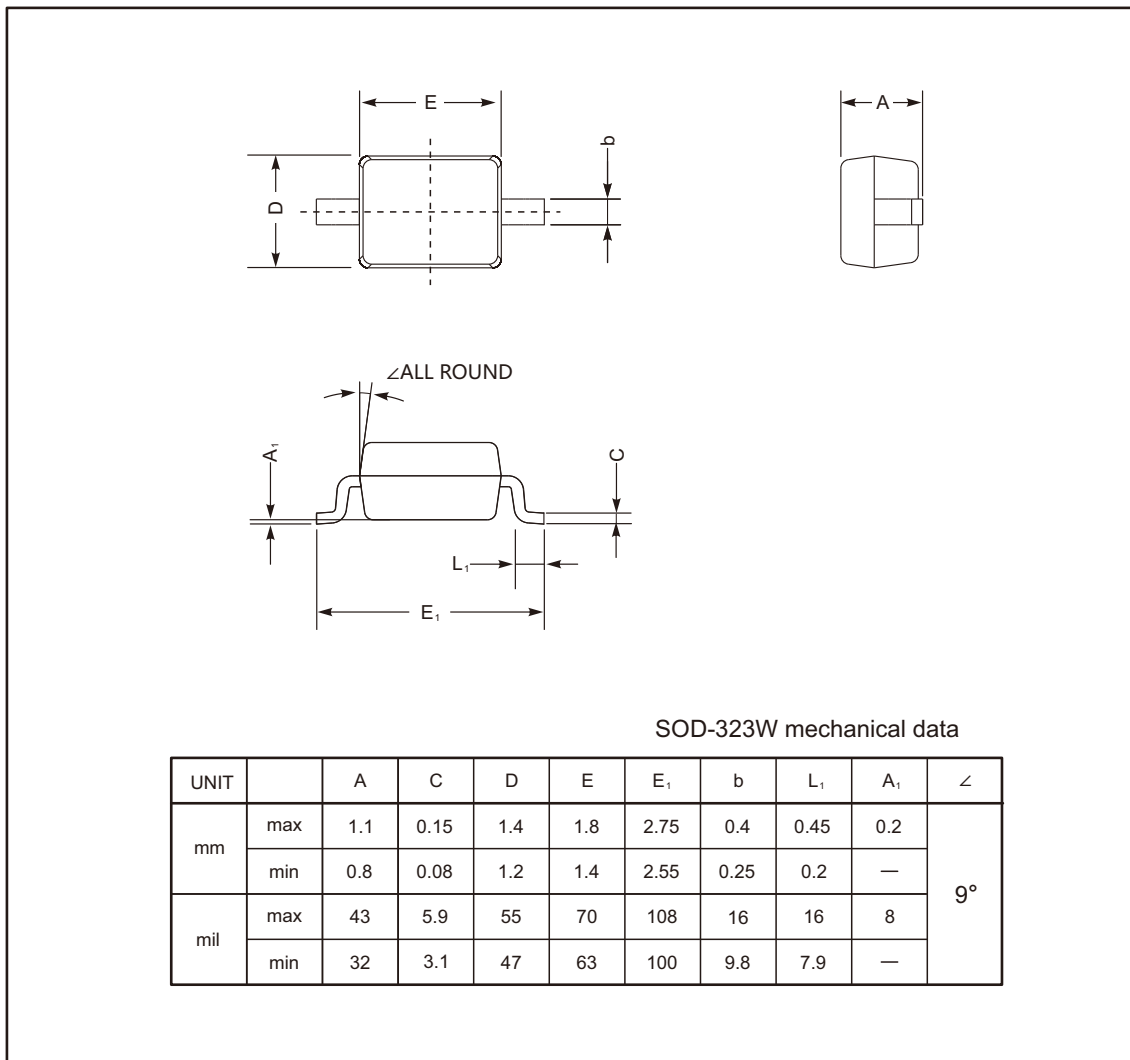




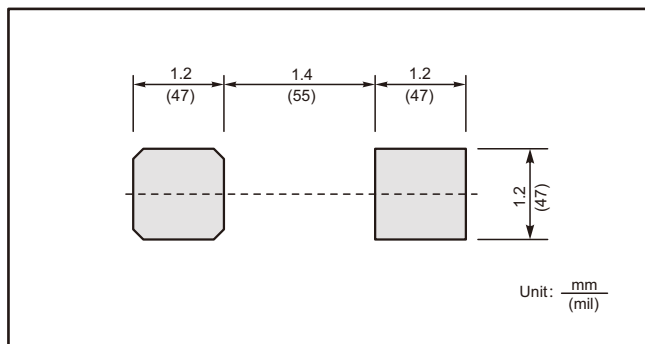
PACKAGE OUTLINE

Plastic surface mounted package; 2 leads

SOD-323W



The recommended mounting pad size



Marking

| Type number | Marking code |
|---------------|--------------|
| ESDBLC5V0D3LP | 5Z |



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